



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Harold C. Moore

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January 16, 2007

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Re:	Application of:	Charles E. May
	Serial No.:	09/943,196
	Filed:	August 30, 2001
	For:	Arrangement and Method for Fabricating A Semiconductor Wafer
	Group Art Unit:	1765
	Examiner:	Lynette T. Umez-Eronini
	Confirmation No.:	8500
	Our Docket No.:	01-146/RCE (1003-0606)

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated September 14, 2006, please consider the
following response.